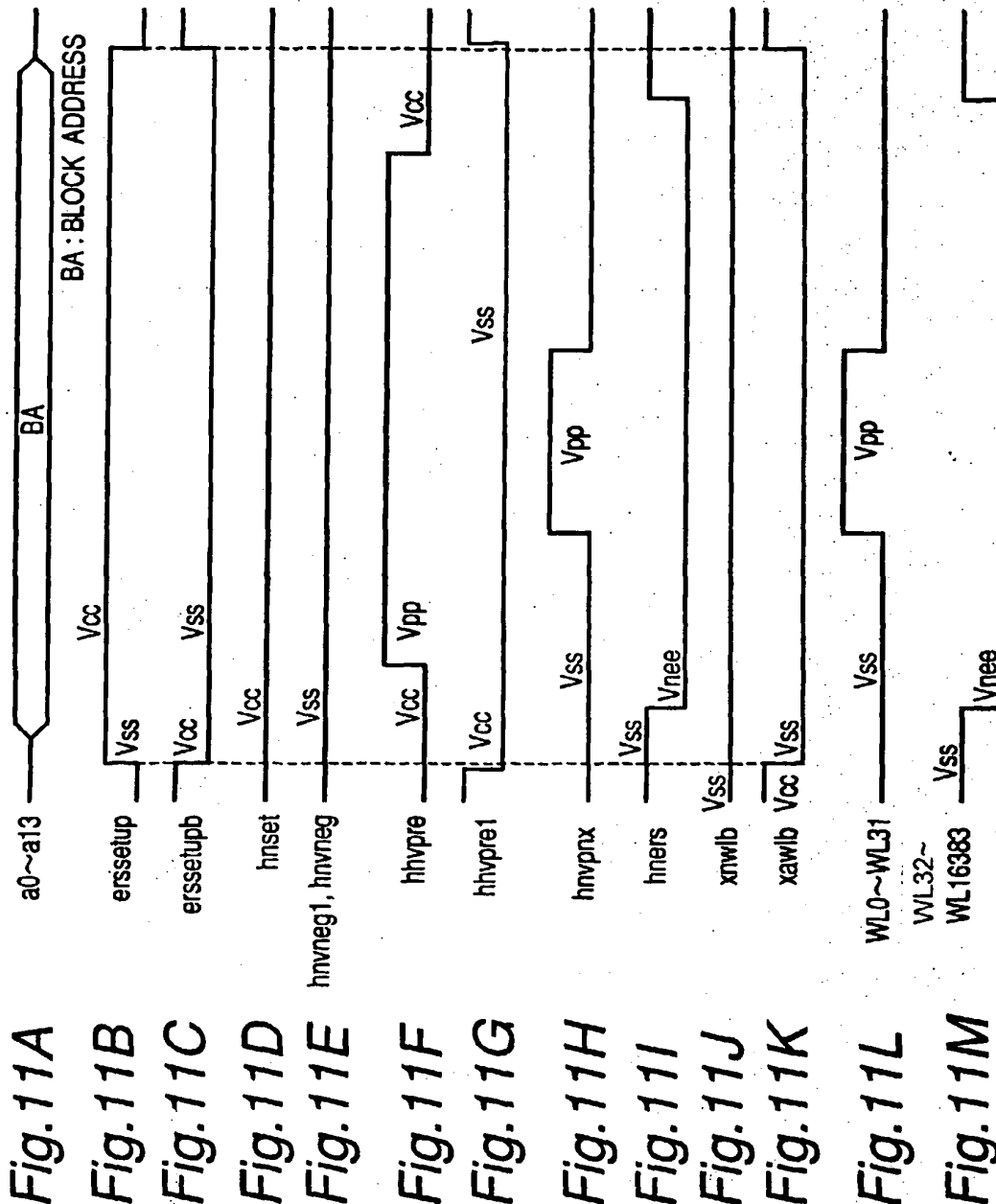




Approved
 10/17/03

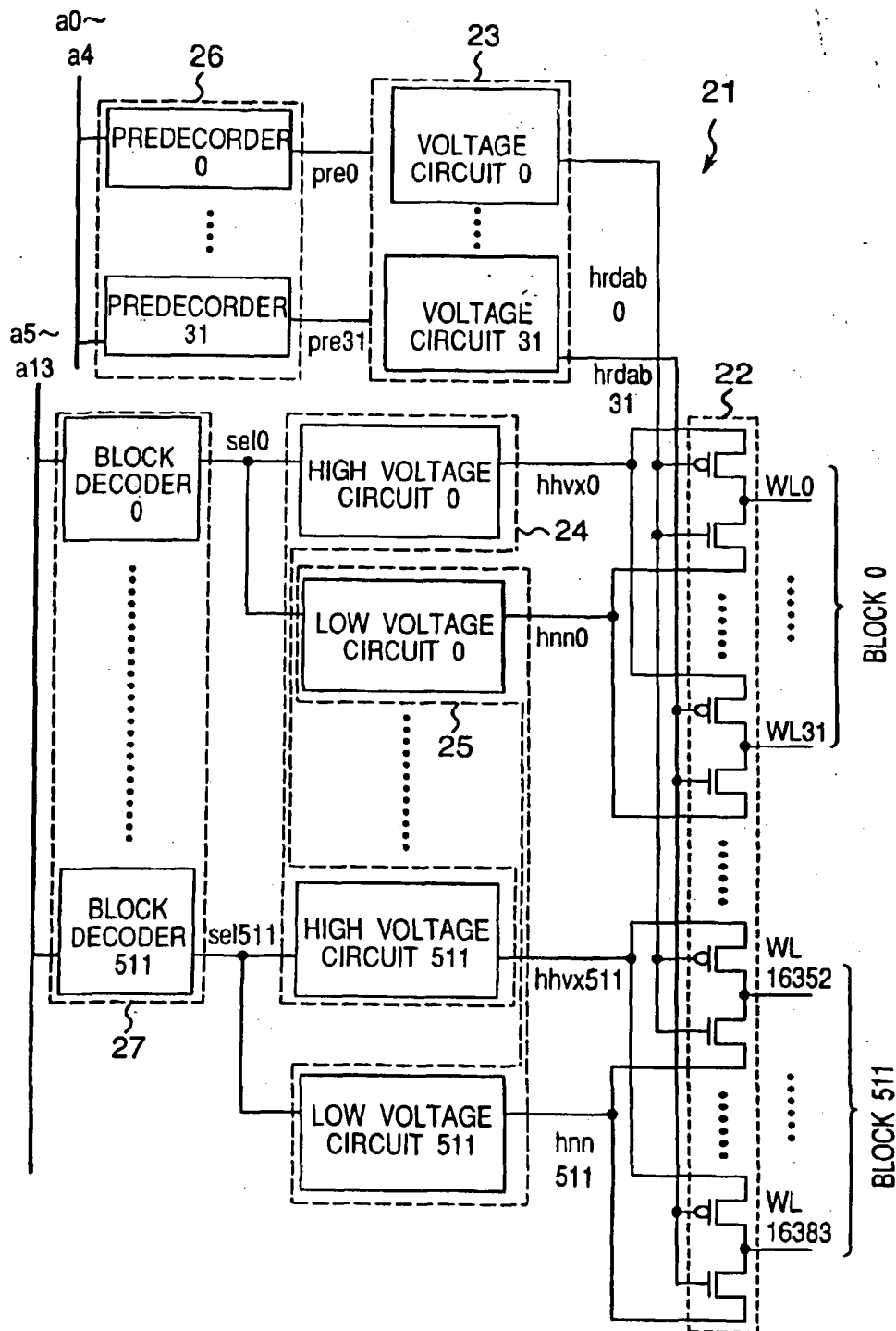
REPLACEMENT SHEET



01
SEP 08 2003
PATENT & TRADEMARK OFFICE

App No.: 09/598,384
Inventor: Yasuaki HIRANO
Title: ERASE METHOD FOR NONVOLATILE
SEMICONDUCTOR STORAGE DEVICE AND ROW
DECODER CIRCUIT FOR FULFILLING THE METHOD

Fig.12 **REPLACEMENT SHEET**



07P
SEP-08 2003
J. HASEGAWA OFFICE

App No.: 09/598,384

Docket No.: 204552018400

Inventor: Yasuaki HIRANO

Title: ERASE METHOD FOR NONVOLATILE
SEMICONDUCTOR STORAGE DEVICE AND ROW
DECODER CIRCUIT FOR FULFILLING THE METHOD

REPLACEMENT SHEET

Fig. 13

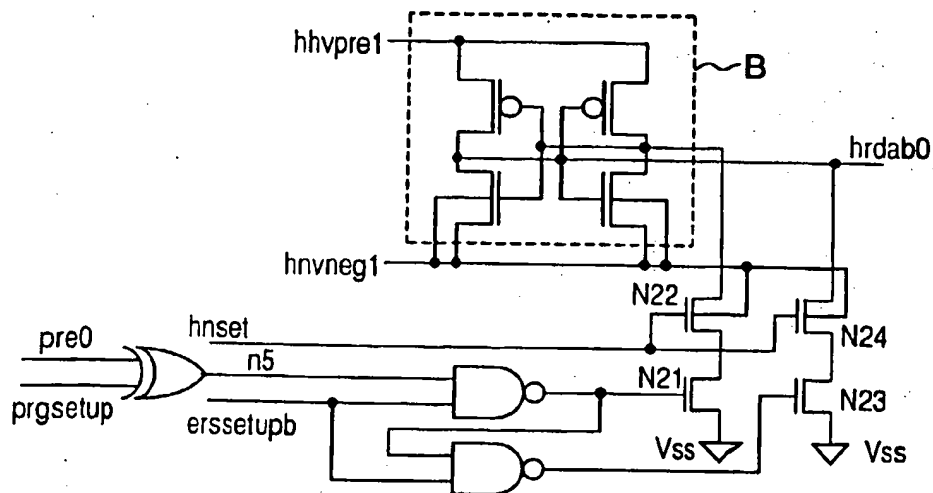
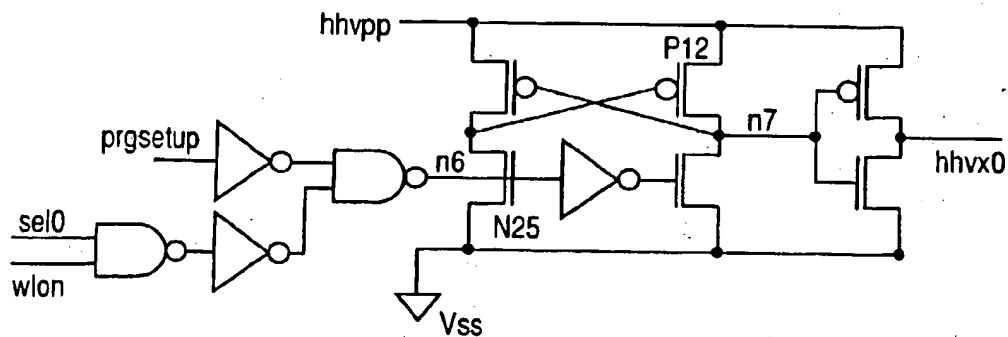


Fig. 14





REPLACEMENT SHEET

Fig.19 PRIOR ART

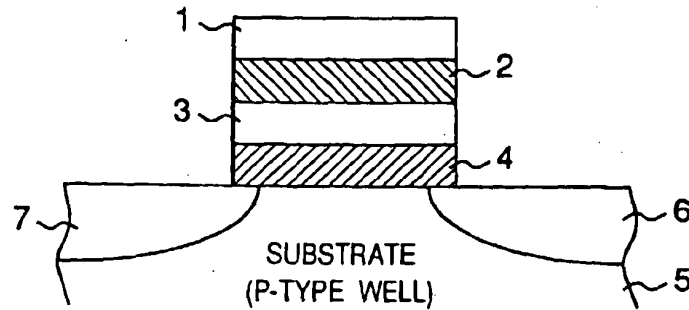
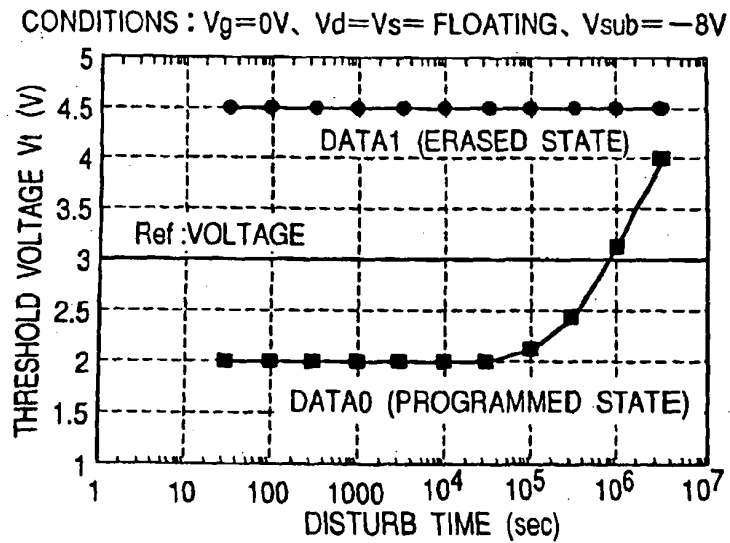


Fig.20 PRIOR ART





App No.: 09/598,384

Docket No.: 204552018400

Inventor: Yasuaki HIRANO

Title: ERASE METHOD FOR NONVOLATILE
SEMICONDUCTOR STORAGE DEVICE AND ROW
DECODER CIRCUIT FOR FULFILLING THE METHOD

REPLACEMENT SHEET

Fig.21 PRIOR ART

